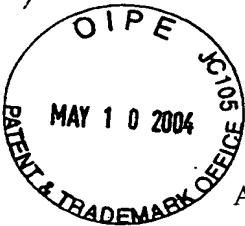


PATENT
Docket No. 20002/16347

IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE



Applicant: Kiran R. Desai

) I hereby certify that this paper is
being deposited with the United
States Postal Service with
sufficient postage as first class
mail in an envelope addressed to:
Commissioner for Patents, P.O.
Box 1450, Alexandria, VA 22313-
1450 on this date:

Serial No.: 10/630,465

May 5, 2004

Filed: July 30, 2003


Mark C. Zimmerman
Registration No. 44,006
Attorney for Applicant(s)

For: "Methods and Apparatus for Filtering
a Cache Snoop"

Group Art Unit: 2186

Examiner: Unknown

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The patents and publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents and publications are enclosed.

TIME OF FILING

This information disclosure statement is being filed, to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

No fee is required.

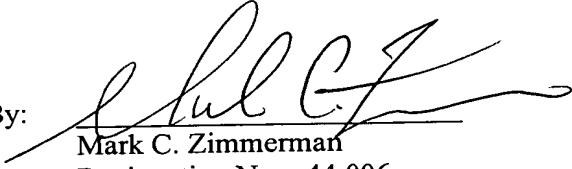
The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

GROSSMAN & FLIGHT, LLC.
20 N. Wacker Drive
Suite 4220
Chicago, Illinois 60606
(312) 580-1020

By:



Mark C. Zimmerman
Registration No.: 44,006

May 5, 2004

Attorneys for Intel Corporation

Form PTO-1449 (Modified)

U.S. Department of Commerce
Patent and Trademark Office

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

| | |
|-------------------------------------|--------------------------|
| Atty. Docket No. 20002/16347 | Serial No. 10/630,465 |
| Applicant Kiran R. Desai | |
| Filing Date July 30, 2003 | Group Art Unit 2186 |

U.S. PATENT DOCUMENTS

| *EXAMINER INITIALS | | DOCUMENT NUMBER | ISSUE DATE | INVENTOR(S) | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|--------------------|-----|-----------------|------------|-----------------|-------|----------|----------------------------|
| | A01 | 6,598,123 | 07/22/2003 | Anderson et al. | 711 | 133 | |
| | A02 | 6,704,845 | 03/09/2004 | Anderson et al. | 711 | 146 | 04/14/2003 |

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

| | |
|-----|---|
| C01 | Tendler, Joel M. and Steve Dodson, Steve Fields, Hung Le, Balaram Sinharoy. "IBM eServer: Power4 System Microarchitecture." October, 2001. Available at http://www-1.ibm.com/servers/eserver/pseries/hardware/whitepapers/power4.pdf . |
|-----|---|

| EXAMINER | DATE CONSIDERED |
|--|-----------------|
| *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to applicant. | |